

**Kingdom of Saudi Arabia**

**The National Commission for Academic Accreditation &  
Assessment**

**COURSE SPECIFICATION**

**14031201-4 Digital Logic Design**

# Course Specification

Institution	<b>Umm Al-Qura University</b>
College/Department	<b>College of Computer &amp; Information Systems</b>

## A Course Identification and General Information

1. Course title and code:	Digital <b>Logic Design 14031201-4</b>
2. Credit hours	<b>4</b>
3. Program(s) in which the course is offered. (If general elective available in many programs indicate this rather than list programs)	
4. Name of faculty member responsible for the course	<b>Dr. Khalid Khayyat</b>
5. Level/year at which this course is offered	<b>Level 3 / Year 2</b>
6. Pre-requisites for this course (if any)	<b>4800153-3</b>
7. Co-requisites for this course (if any)	
8. Location if not on main campus	<b>Al-Abidiyah Umm Al Qura University - Makkah Al Mukarramah</b>

## B Objectives

<p>1. Summary of the main learning outcomes for students enrolled in the course.</p> <p>Number Systems; Binary arithmetic; Boolean/Logic functions; Boolean Algebra; logic gates; function minimization, analysis and synthesis of combinational and sequential circuits.</p>
<p>2. Briefly describe any plans for developing and improving the course that are being implemented. (eg increased use of IT or web based reference material, changes in content as a result of new research in the field)</p> <p>1. Involving students in presentation of advance topics in logic design to know current research in the field.</p> <p>2. Use of Simulation tools to assist students in carrying out the major steps in the circuit design flow.</p> <p>3. Lecture slides and tutorials</p>

## C. Course Description (Note: General description in the form to be used for the Bulletin or Handbook should be attached)

1 Topics to be Covered		
List of Topics	No of Weeks	Contact hours
Introduction to digital design	1	3
Number Systems and Codes	1,2	6
Logic Gates	3,4,5	9
Boolean Algebra and Logic Simplification	6,7,8	9
Combinational Logic Analysis	9,10,11	9
Functions of Combinational Logic	12,13,14	9
Sequential logic : design and analysis	15,16	6

Finite State Machine (FSM) Synthesis	14	3
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2 Course components (total contact hours per semester):				
Lecture: 48hr	Tutorial:	Laboratory 16hr	Practical/Field work/Internship	Other:

3. Additional private study/learning hours expected for students per week. (This should be an average: for the semester not a specific requirement in each week)  3 x 50 mins lectures
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4. Development of Learning Outcomes in Domains of Learning  For each of the domains of learning shown below indicate: <ul style="list-style-type: none"> <li>• A brief summary of the knowledge or skill the course is intended to develop;</li> <li>• A description of the teaching strategies to be used in the course to develop that knowledge or skill;</li> <li>• The methods of student assessment to be used in the course to evaluate learning outcomes in the domain concerned.</li> </ul>
<b>a. Knowledge</b>
(i) Description of the knowledge to be acquired <ol style="list-style-type: none"> <li>1. After the completion of the course, students will know the number systems, Boolean algebra and simplification techniques for digital logic circuits.</li> <li>2. Student will be able to apply the knowledge obtained in steps 1 in design of combinational and sequential circuits.</li> </ol>
(ii) Teaching strategies to be used to develop that knowledge <ol style="list-style-type: none"> <li>1. Assignments and solutions to the assignments, so that student can know their problems</li> <li>2. Open-communication with students – show willingness to assist and take questions from students and clarify explanations in the class</li> </ol>

<ul style="list-style-type: none"> <li>3. Students presentations</li> <li>4. Real-life examples: allows analysis of real-world scenarios</li> </ul>
<p>(iii) Methods of assessment of knowledge acquired</p> <ul style="list-style-type: none"> <li>1. Exercises &amp; Home works, Quizzes, Midterm, and Final Exam</li> <li>2. Review outputs from the assignments in the computer lab and also from their assignments.</li> </ul>
<p><b>b. Cognitive Skills</b></p>
<p>(i) Description of cognitive skills to be developed</p> <ul style="list-style-type: none"> <li>1. Ability to solve problems related to digital systems.</li> <li>2. Ability of deduction and inference.</li> <li>3. Ability of analysis and design of different digital circuits</li> </ul>
<p>(ii) Teaching strategies to be used to develop these cognitive skills</p> <ul style="list-style-type: none"> <li>1. Assignments.</li> <li>2. Labs</li> </ul>
<p>(iii) Methods of assessment of students cognitive skills</p> <ul style="list-style-type: none"> <li>1. Mid and Final Exams</li> <li>2. Labs Exams.</li> </ul>
<p><b>c. Interpersonal Skills and Responsibility</b></p>
<p>(i) Description of the interpersonal skills and capacity to carry responsibility to be developed</p> <p>Students should be able to</p> <ul style="list-style-type: none"> <li>1. Understand and communicate to others the importance and relevance of statistics in the modern world</li> <li>2. Be an independent learner, able to acquire further knowledge with some guidance or support.</li> </ul>

<ul style="list-style-type: none"> <li>3. Participate in group discussions</li> <li>4. Manage time and meet deadlines.</li> </ul>
<p>(ii) Teaching strategies to be used to develop these skills and abilities</p> <ul style="list-style-type: none"> <li>1. Assignments.</li> <li>2. Labs</li> <li>3. Students Presentations</li> </ul>
<p>(iii) Methods of assessment of students interpersonal skills and capacity to carry responsibility</p> <ul style="list-style-type: none"> <li>1. Mid and Final Exams</li> <li>2. Labs Exams.</li> </ul>
<p><b>d. Communication, Information Technology and Numerical Skills</b></p>
<p>(i) Description of the skills to be developed in this domain.</p> <ul style="list-style-type: none"> <li>1. Case studies: the key method of discovering a student's dexterity in analyzing</li> <li>2. Their recommendations, opinions and suggestions</li> <li>3. Assignments, exams, reports, presentations and quizzes will test their analytic skills and communication skills</li> <li>4. Class discussions should indicate a student's prowess in responding</li> </ul>
<p>(ii) Teaching strategies to be used to develop these skills</p> <ul style="list-style-type: none"> <li>1. Written Examinations</li> <li>2. Assignments</li> <li>3. Quizzes</li> </ul>
<p>(iii) Methods of assessment of students numerical and communication skills</p> <ul style="list-style-type: none"> <li>1. Assignments, exams, reports, presentations and quizzes will test their analytic skills and communication skills</li> <li>2. Class discussions should indicate a student's prowess in responding</li> </ul>

<b>e. Psychomotor Skills (if applicable)</b>			
(i) Description of the psychomotor skills to be developed and the level of performance required			
(ii) Teaching strategies to be used to develop these skills			
(iii) Methods of assessment of students psychomotor skills			

5. Schedule of Assessment Tasks for Students During the Semester			
Assessment	Assessment task (e.g. essay, test, group project, examination etc.)	Week due	Proportion of Final Assessment
1	Attendance, Participation and Labs evaluation	Throughout semester	30
2	Quiz	Throughout semester	5
3	Mid Term	10	20
4	Home Work	Throughout semester	5
5	Final Exam	16	40

## D. Student Support

1. Arrangements for availability of teaching staff for individual student consultations and academic advice. (include amount of time teaching staff are expected to be available each week)

1. Faculty is available 10 hours per week for student help and consulting.

## E Learning Resources

### 1. Required Text(s)

1. M. Morris Mano and Charles Kime, Logic and Computer Design Fundamentals, 4th Edition, 2007, Prentice Hall

### 2. Essential References

1. Frank Vahid, Digital Design with RTL Design, VHDL, and Verilog, Second Edition, 2011, John Wiley and Sons.

### 3- Recommended Books and Reference Material (Journals, Reports, etc) (Attach List)

### 4- Electronic Materials, Web Sites etc

<http://uqu.edu.sa/azabid>

<http://www.asic-world.com/digital>

### 5- Other learning material such as computer-based programs/CD, professional standards/regulations

1. Modelsim for simulation of Verilog examples.

## F. Facilities Required

Indicate requirements for the course including size of classrooms and laboratories (ie number of seats in classrooms and laboratories, extent of computer access etc.)

### 1. Accommodation (Lecture rooms, laboratories, etc.)

1. A Lecture room having Multimedia projector for lectures and students presentation.



2. Digital Logic Design Lab.
2. Computing resources <ul style="list-style-type: none"> <li>1. There are computer labs available for development of software skills.</li> <li>2. Students are encouraged to bring in their laptops and use them in solving problems in the class room.</li> </ul>
3. Other resources (specify --e.g. If specific laboratory equipment is required, list requirements or attach list)

## **G Course Evaluation and Improvement Processes**

1 Strategies for Obtaining Student Feedback on Effectiveness of Teaching <ul style="list-style-type: none"> <li>1. Course Survey and students Feedback for each learning outcome of the course.</li> </ul>
2 Other Strategies for Evaluation of Teaching by the Instructor or by the Department <ul style="list-style-type: none"> <li>1. Faculty meetings to discuss best practices and issues related to the course</li> <li>2. Comparison of the course content with similar courses offered in others colleges</li> <li>3. Updating course curriculum according to latest research done in the field.</li> </ul>
3 Processes for Improvement of Teaching <ul style="list-style-type: none"> <li>1. Departmental Meetings</li> </ul>
4. Processes for Verifying Standards of Student Achievement (eg. check marking by an independent member teaching staff of a sample of student work, periodic exchange and remarking of tests or a sample of assignments with staff at another institution) <ul style="list-style-type: none"> <li>1. Departmental Meetings</li> </ul>

5 Describe the planning arrangements for periodically reviewing course effectiveness and planning for improvement.

1. Departmental Meetings and management meetings